A.D.J. DHARMAMBAL POLYTECHNIC COLLEGE, NAGAPATTINAM

(A Govt. Aided Co-Educational Institution)

INTERNAL EXAMINATION – I **JANUARY 2024**

OVERALL TIME TABLE

INTERNAL EXAM CELL - CIRCULAR Dated: 24.01.24

Time: 2.30 nm to 4.30 nm

Time: 2.30 nm to 4.30 nm

I YEAR - II SEMESTER

I I DAK I	BENIESTER	1 me: 2.30 pm to 4.30 pm	
Date/Dept.	29.01.24	30.01.24	31.01.24
I YEAR	Tamils and Technology	Basics of Engineering	Communicative English

II YEAR-IV SEMESTER Time: 2.30 pm to 4.30 pm					
Date/Dept.	29.01.24	30.01.24	31.01.24	01.02.24	
ECE	Industrial Electronics	Communication	Analog and Digital	E-Vehicle Technology	
		Engineering	Electronics		
CE	Web Design and	Computer Architecture	Object Oriented	Relational Database	
	Programming		Programming With Java	Management System	
ICE	Analog and Digital	Measurement of Process	Measurements and	E-Vehicle Technology	
	Electronics	Variables	Instruments		
EEE	Electrical Machines - II	Measurements, Instruments	Analog and Digital	E-Vehicle Technology and	
	Electrical Wachines - II	and Transducers	Electronics	Policy	
MECH	Fluid Mechanics & Fluid	Manufacturing Technology II	Electrical Drives and	Production and Quality	
	Power	Wianuracturing reciniology if	Controls	Management	

III VEAR VI SEMESTER

III TEAK-VI SEIVIESTEK			1 me. 2.30 pm to 4.30 pm
Date/Dept.	29.01.24	30.01.24	31.01.24
ECE	Computer Hardware Servicing and Networking	Bio Medical Instrumentation	Embedded Systems
CE	Computer Hardware and Servicing	Multimedia Systems	Computer Network and Security
ICE	Power Plant Instrumentation	Bio Medical Instrumentation	Industrial Automation and Drives
EEE	Distribution And Utilization	Energy Conservation & Audit	Power Electronics

COE **PRINCIPAL**

Signature of HOD's

1. ECE	2. CE	3. ICE	4. EEE	5. MECH	6. I YEAR